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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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|--|---------------------------|---|---------------|----------------------------------|--------------------------|
| APPL NUM 10032982 | FILING DATE 12/28/2001 | CLASS 324 | SUBCLASS - | GAU 2856 | EXAMINER V. T. GUNYAN |
| **APPLICANTS: Patel Gunvant; | | | | | |
| **CONTINUING DATA VERIFIED: None/V.N | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: None/V.N | | | | | |
| PG-PUB <input type="checkbox"/> | | DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | |
| Foreign priority claimed | | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | | ATTORNEY DOCKET NO | |
| 35 USC 119 conditions met | | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no | | TI-33382 | |
| Verified and Acknowledged Examiners's initials | | V. T. Gunyan | | | |
| TITLE : Method and system for conducting continuity testing on analog devices having sensitive input nodes | | | | | |
| U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94) | | | | | |

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|--|-----------|---------------------------|-------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | |
| | | Print Claim for O.G | |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. |
| | | Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Application Examiner | |
| | | PREPARED FOR ISSUE | |
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